

1/23

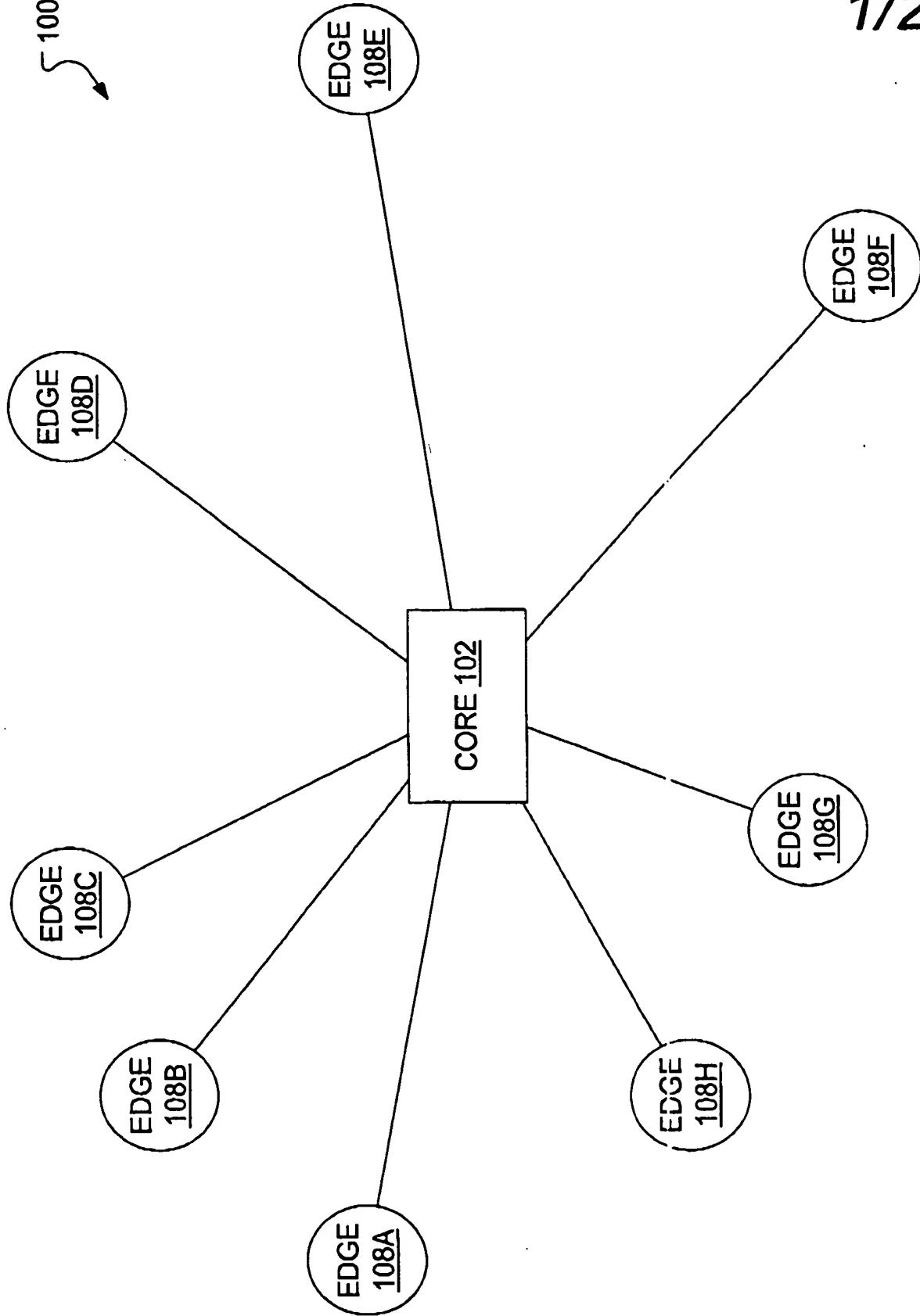
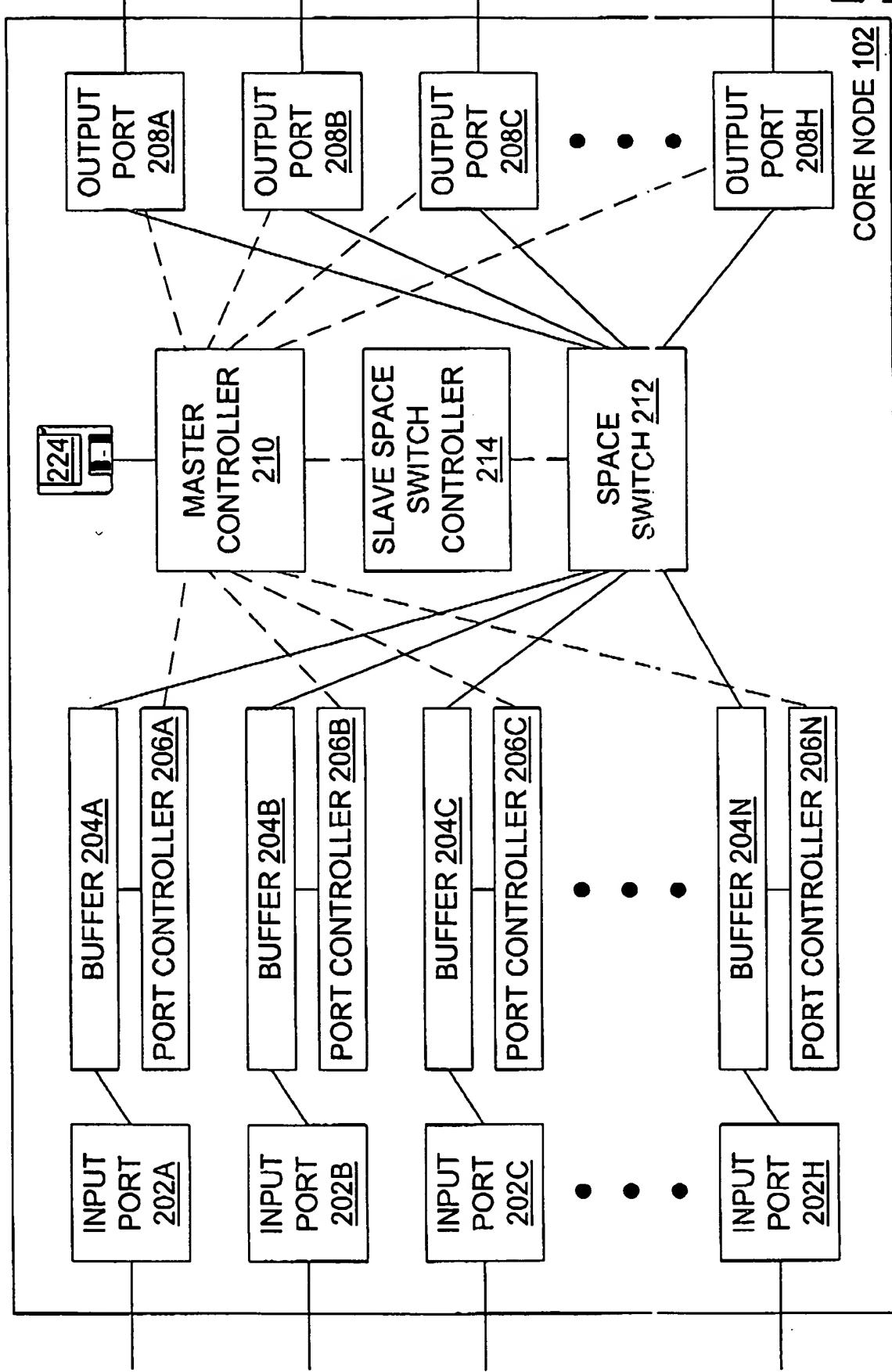


FIG. 1

13244ROUS01U

13244ROUS01U

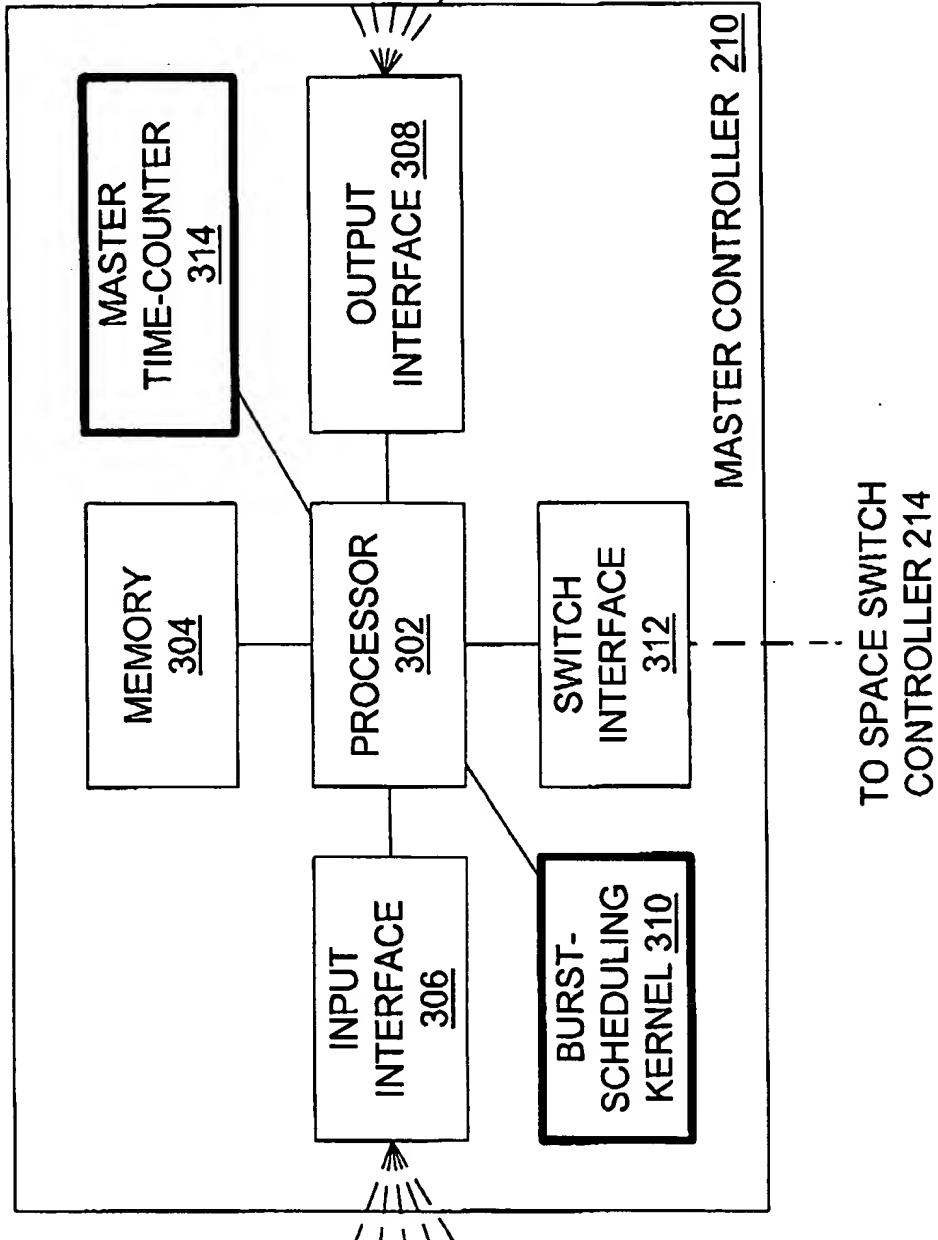


2/23

FIG. 2

FROM OUTPUT PORTS 208

3/23

FIG. 3

TO/FROM PORT CONTROLLERS 206

4/23

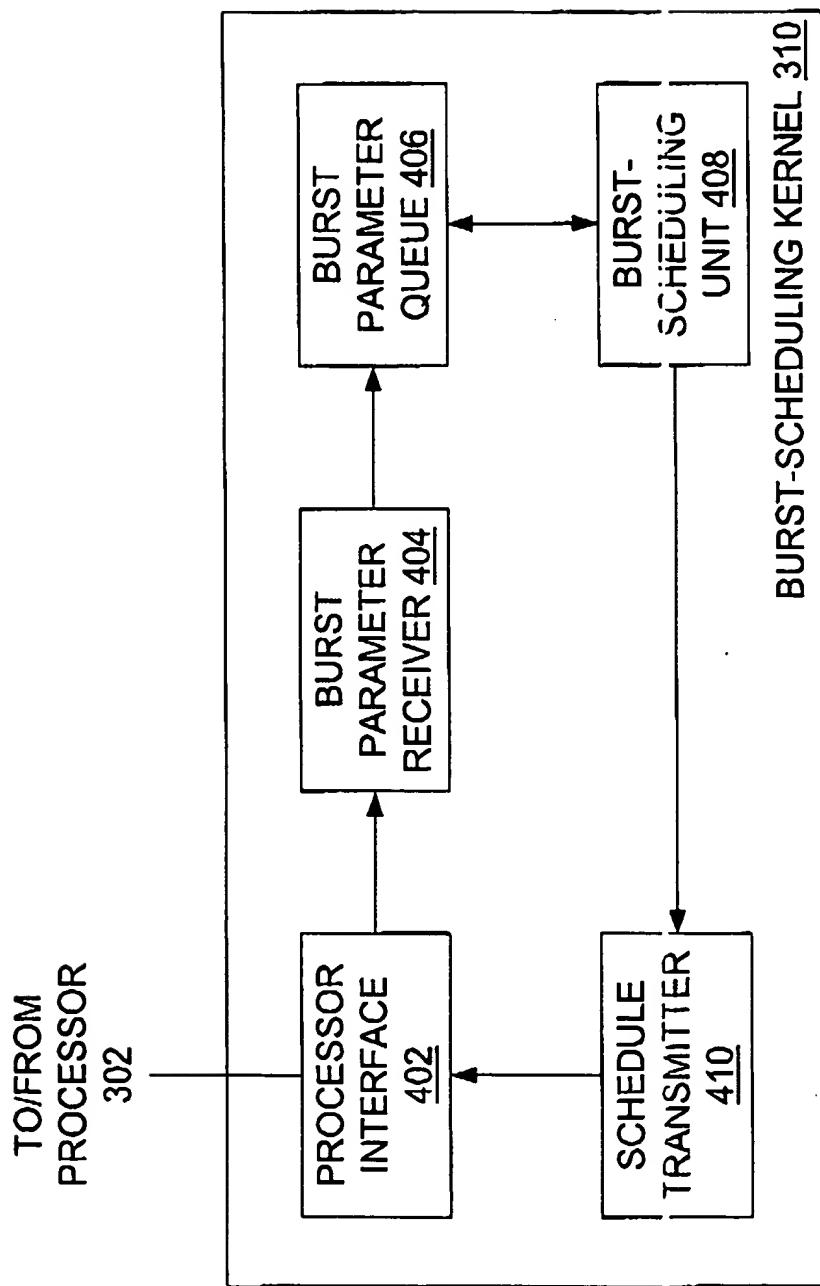


FIG. 4

5/23

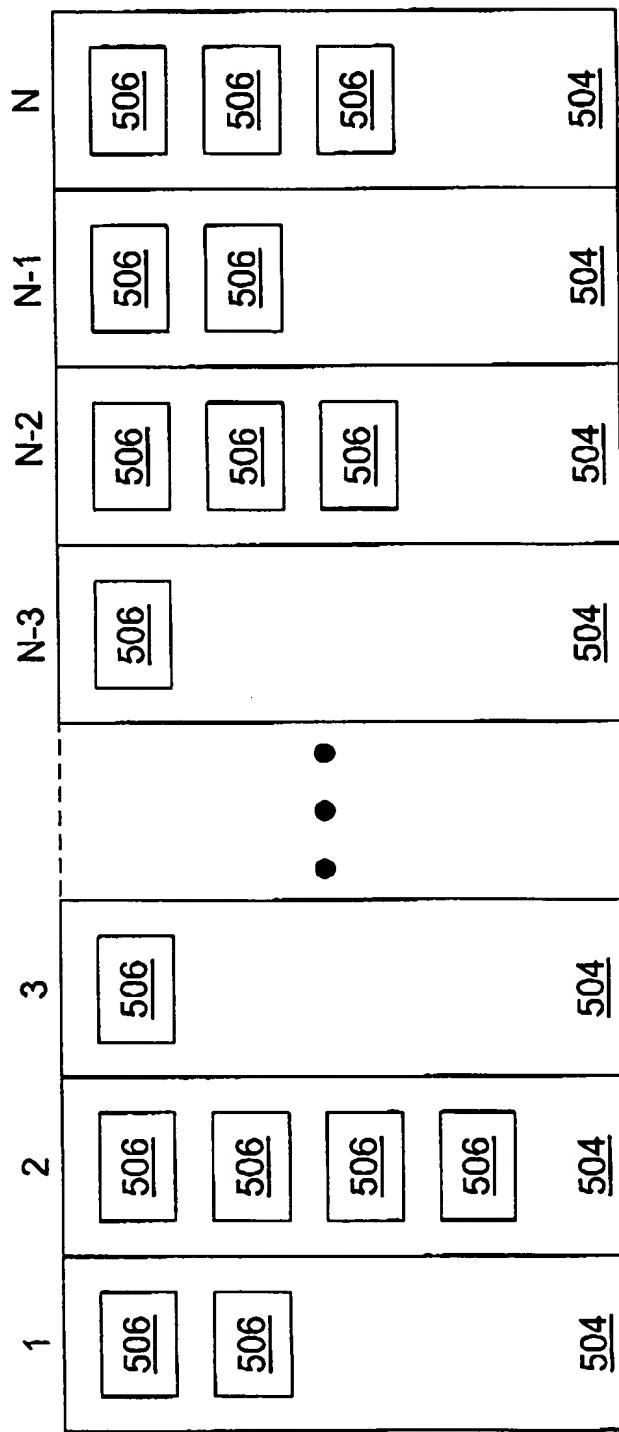


FIG. 5A

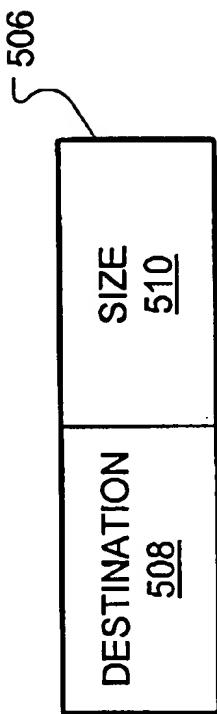


FIG. 5B

6/23

	1	2	3	K-3	K-2	K-1	K
				NULL 608	ID 606	NULL 608	NULL 606
						604	604
							604

600 ↗

FIG. 6

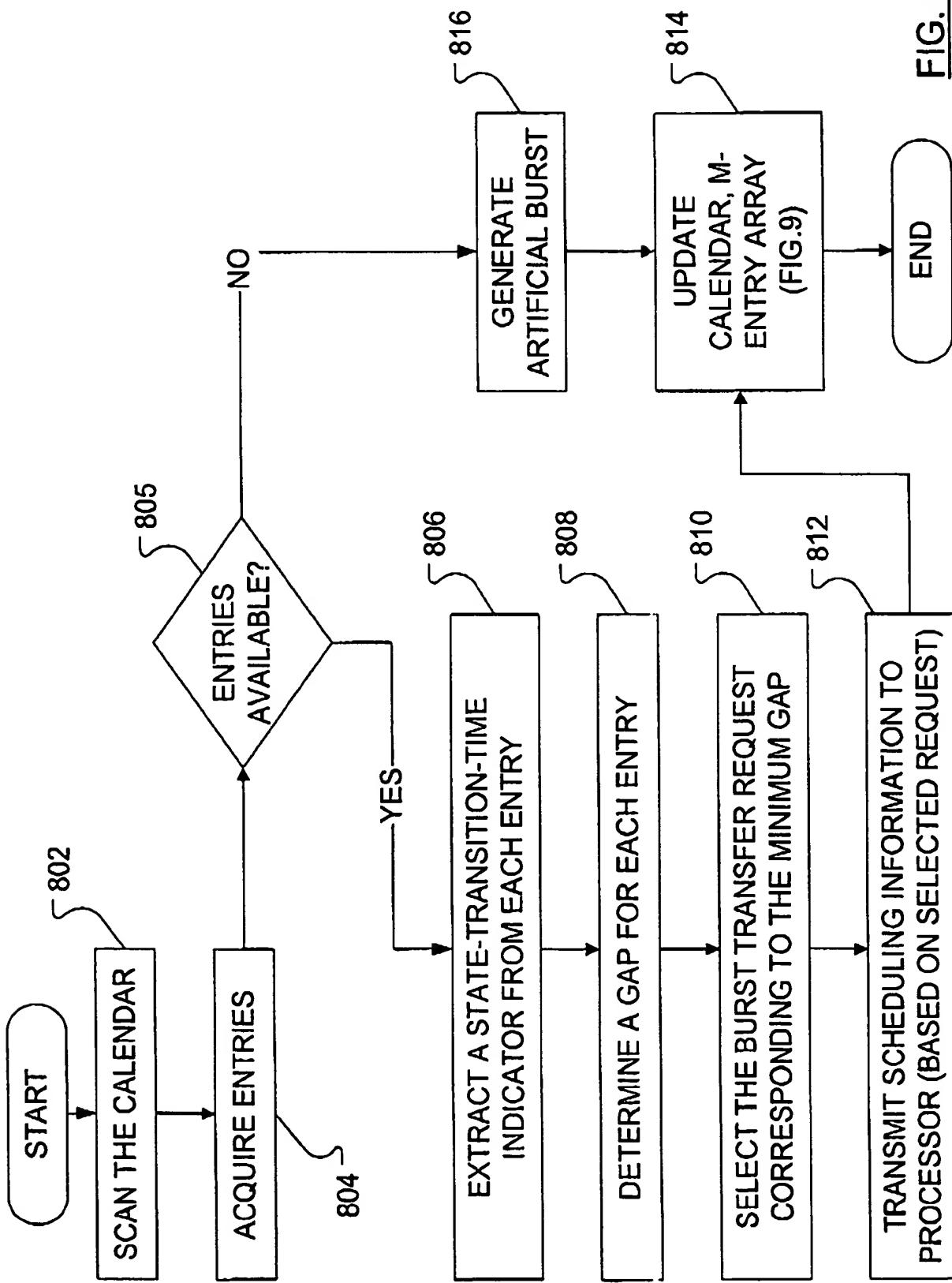
7/23

	1	2	3	M-3	M-2	M-1	M
				<u>706</u>	<u>706</u>	<u>706</u>	<u>706</u>
				<u>706</u>	<u>706</u>	<u>704</u>	<u>704</u>
				<u>706</u>	<u>704</u>	<u>704</u>	<u>704</u>
				<u>704</u>	<u>704</u>	<u>704</u>	<u>704</u>

700 ↗

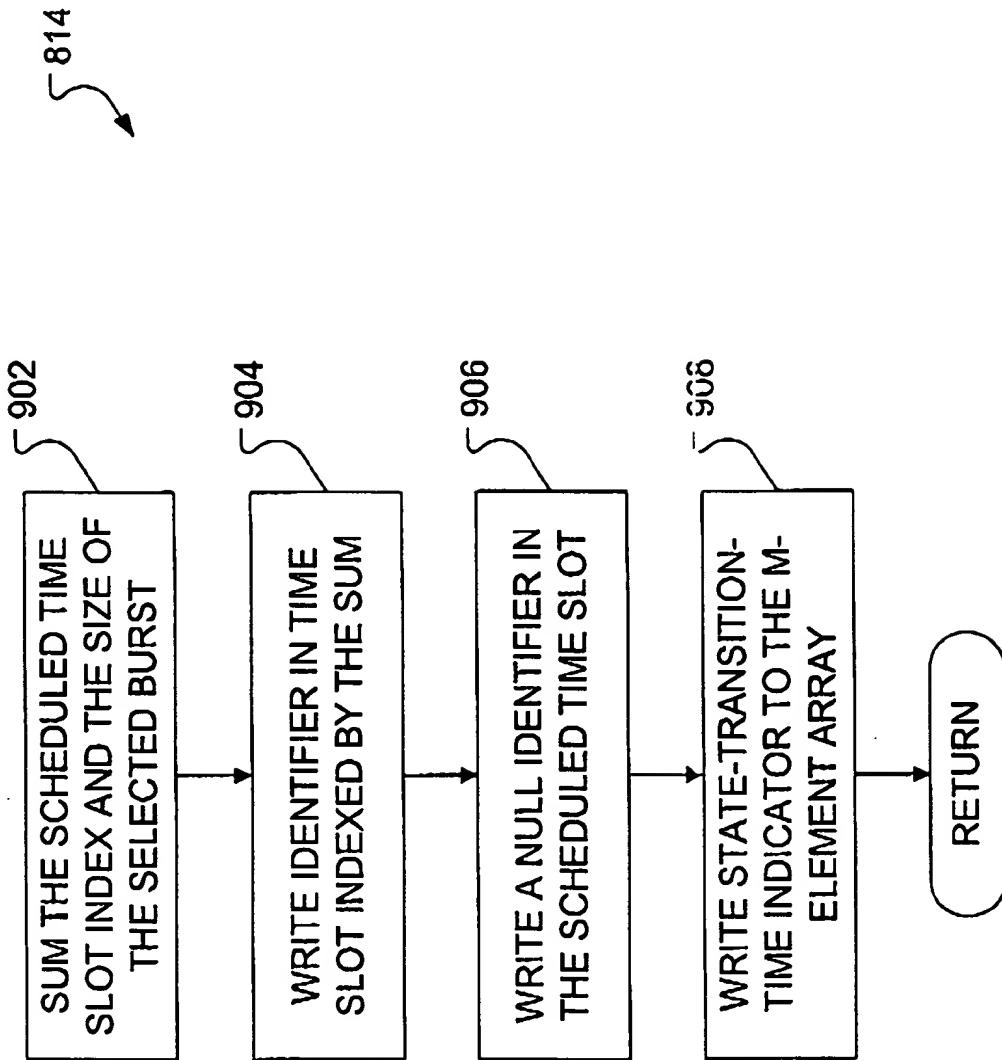
FIG. 7

8/23



13244ROUS01U

9/23

FIG. 9

10/23

13244ROUS01U

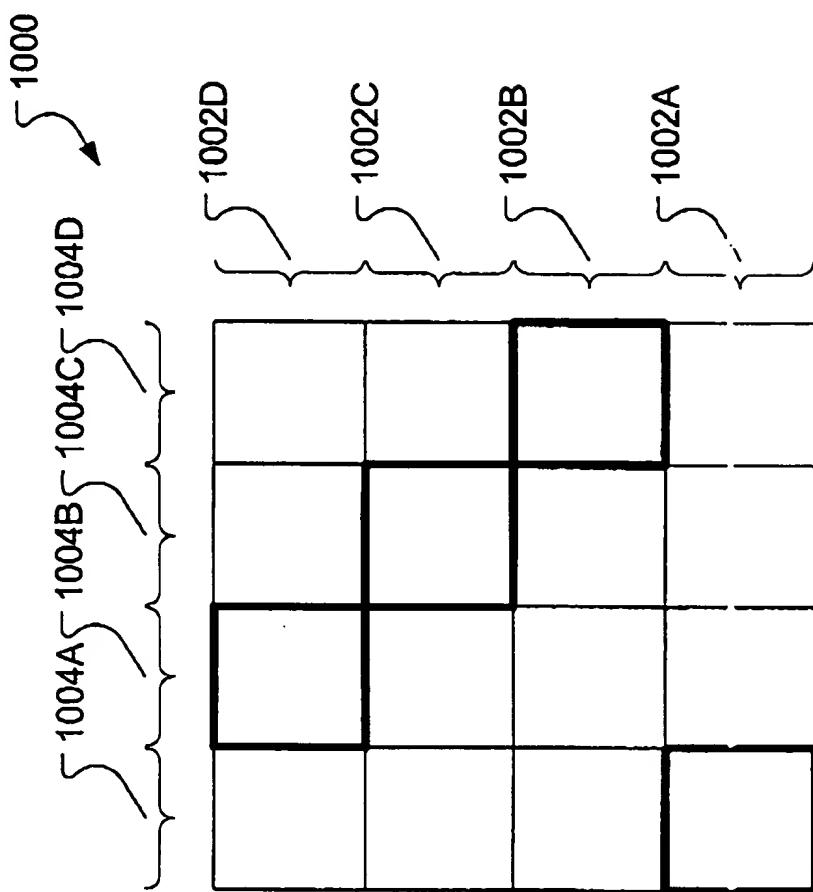
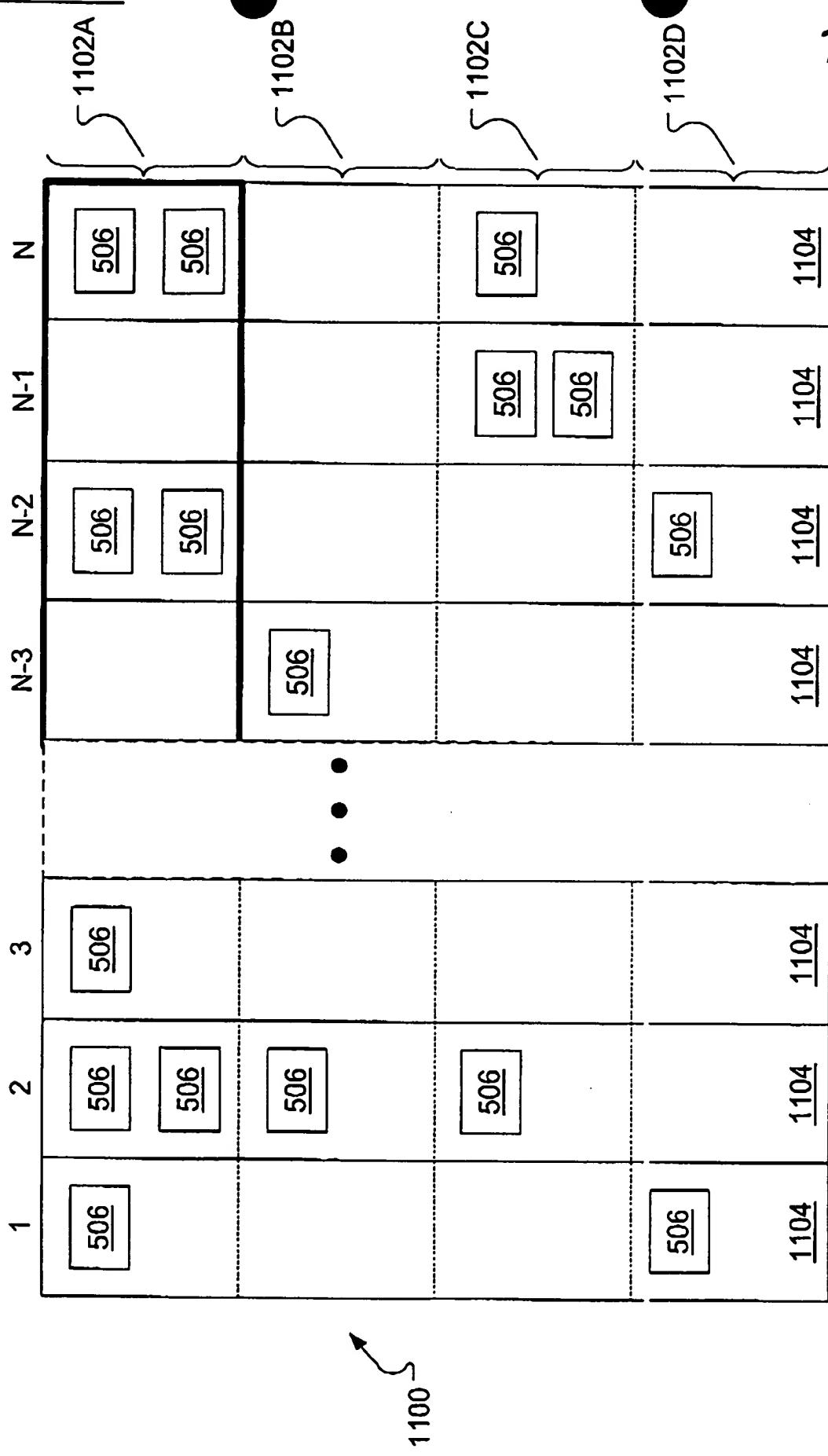
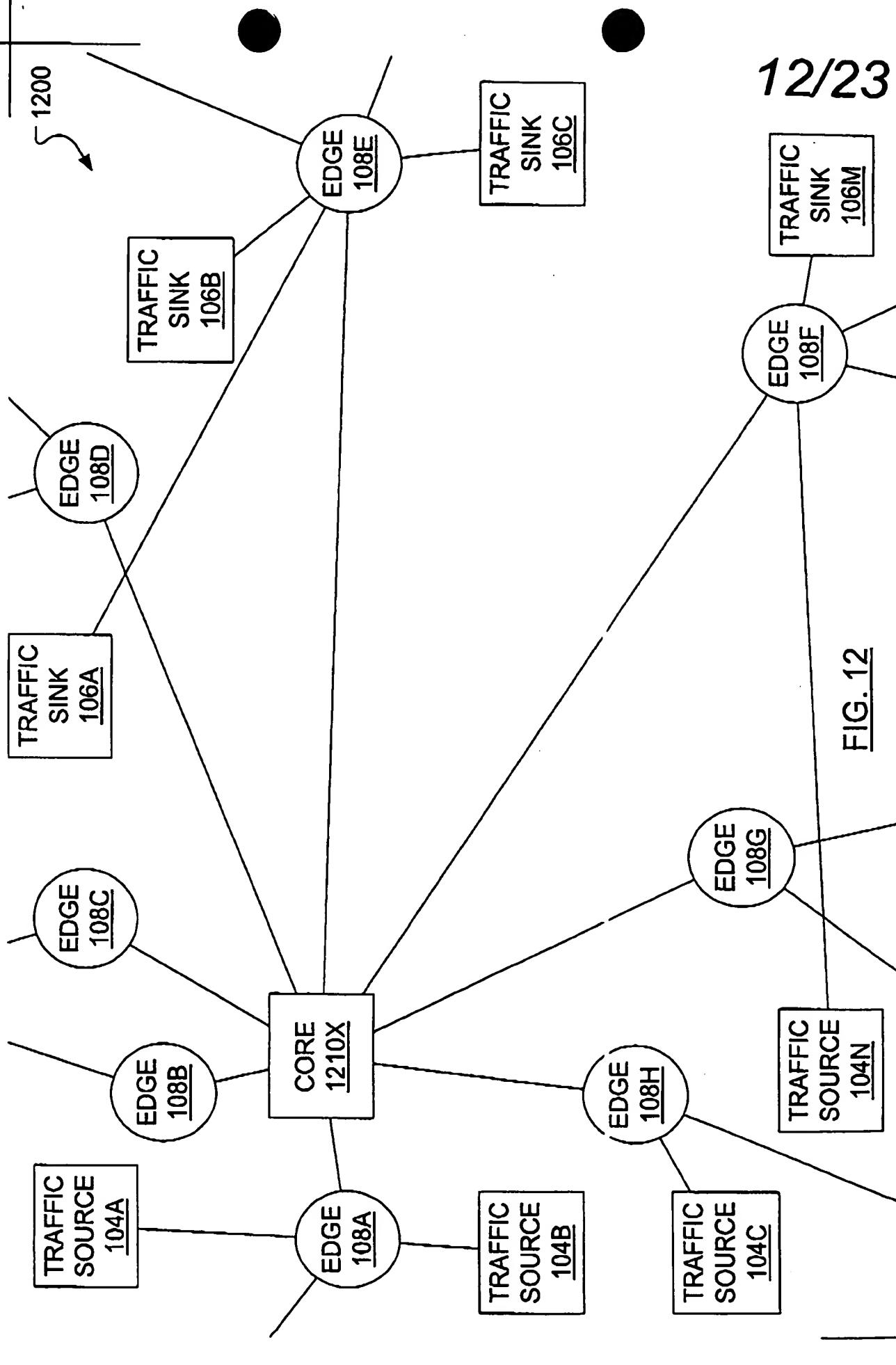


FIG. 10



11/23

FIG. 11



13/23

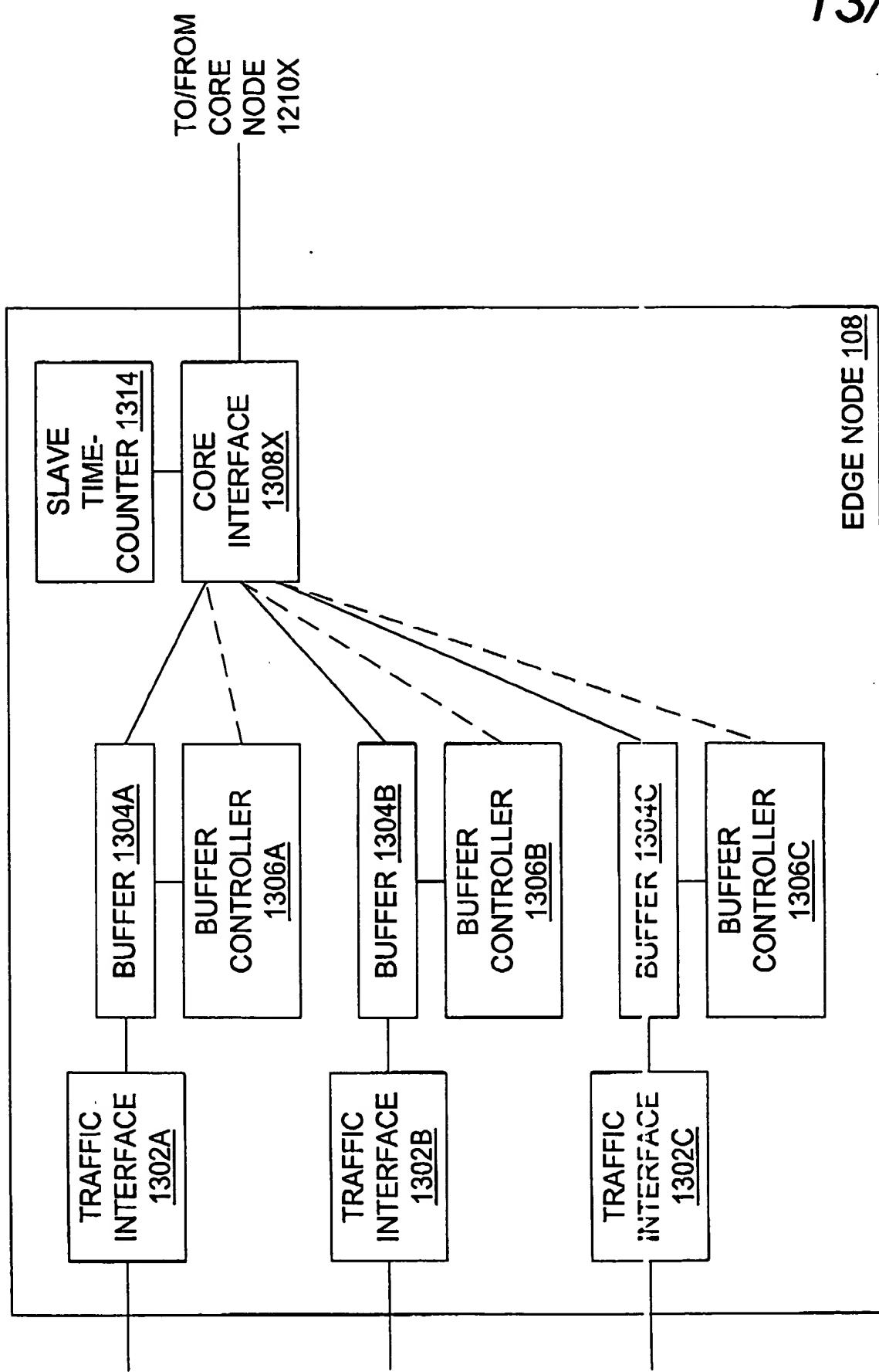


FIG. 13

14/23

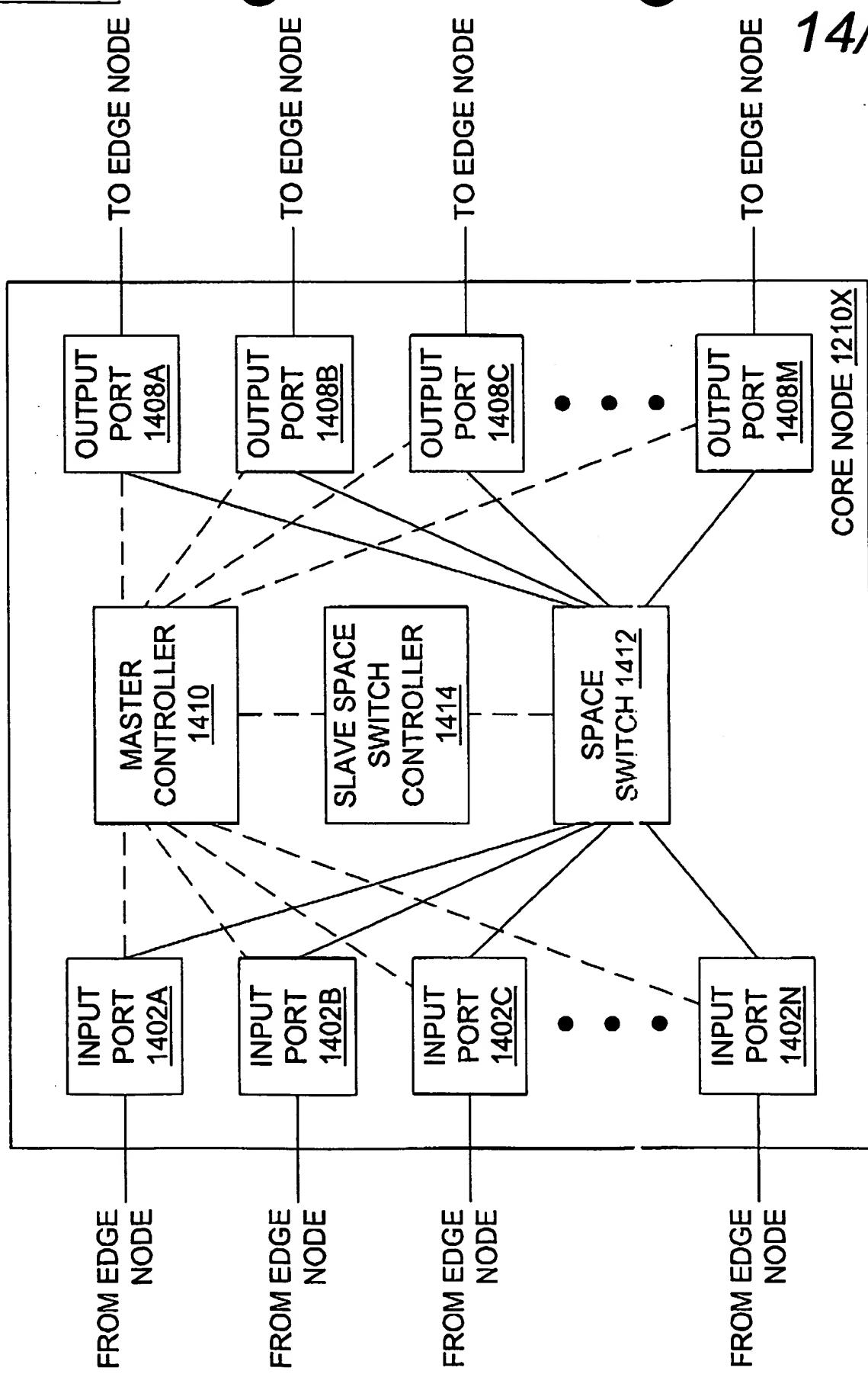
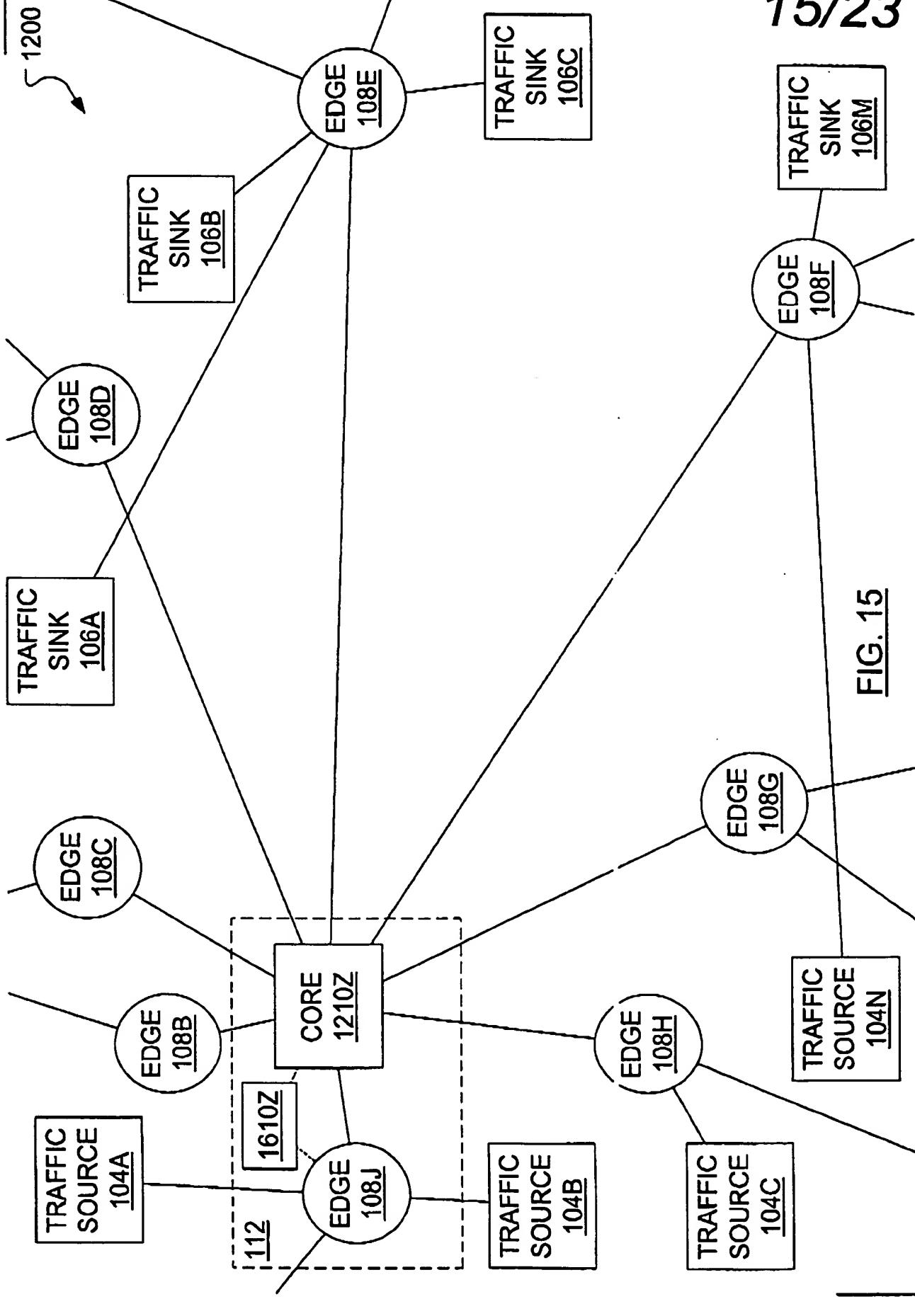


FIG. 14



16/23

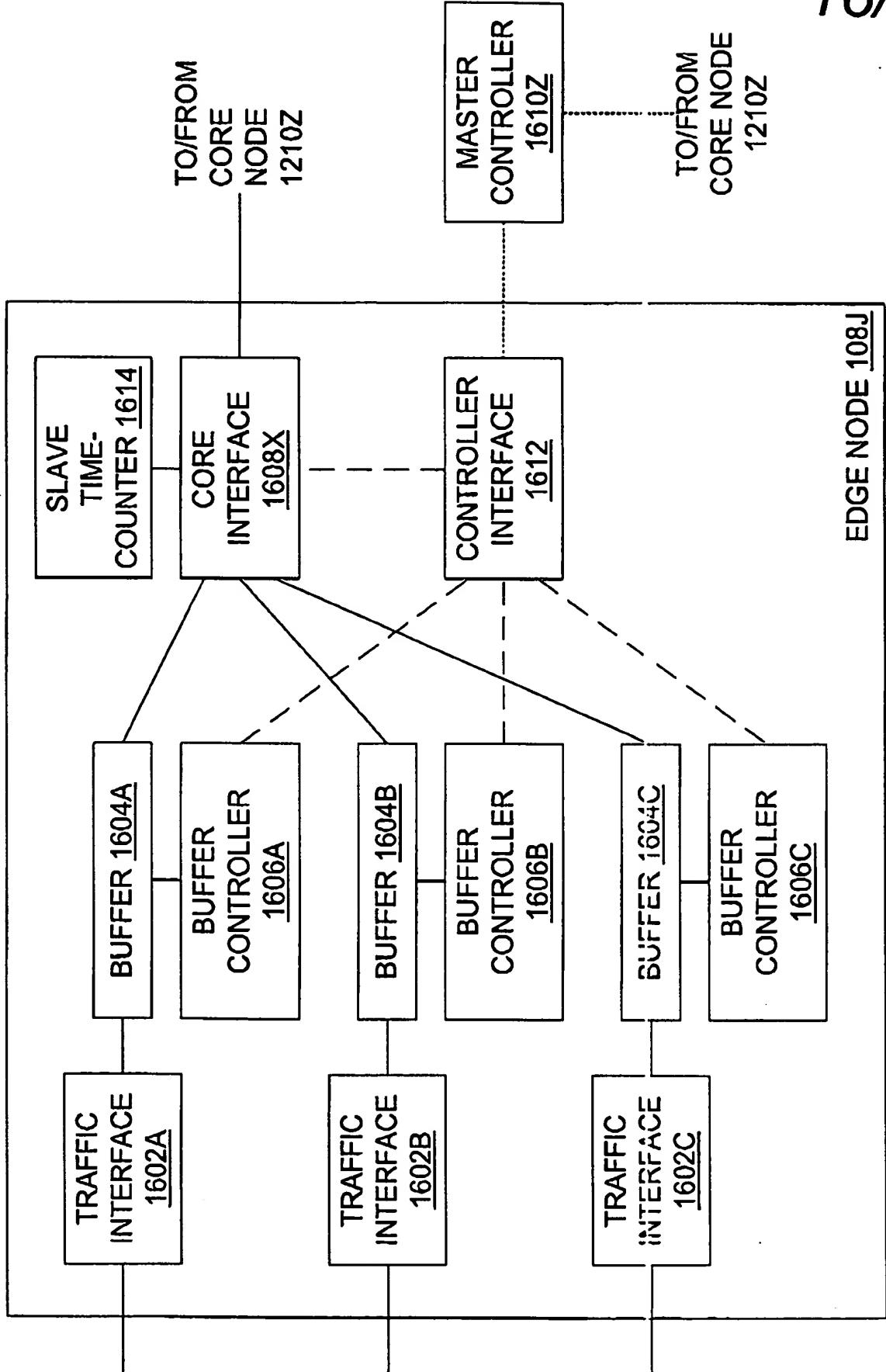


FIG. 16

17/23

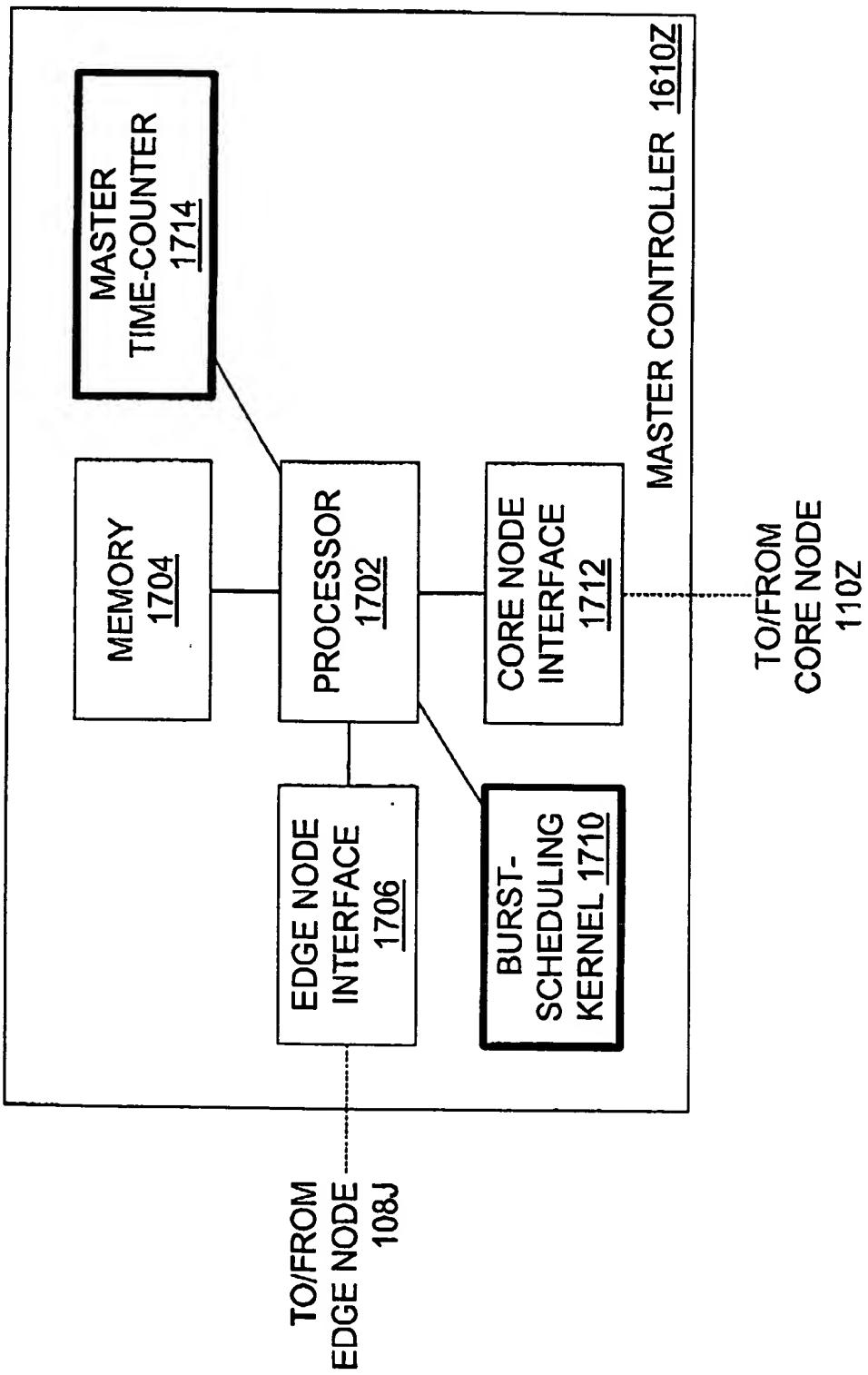


FIG. 17

18/23

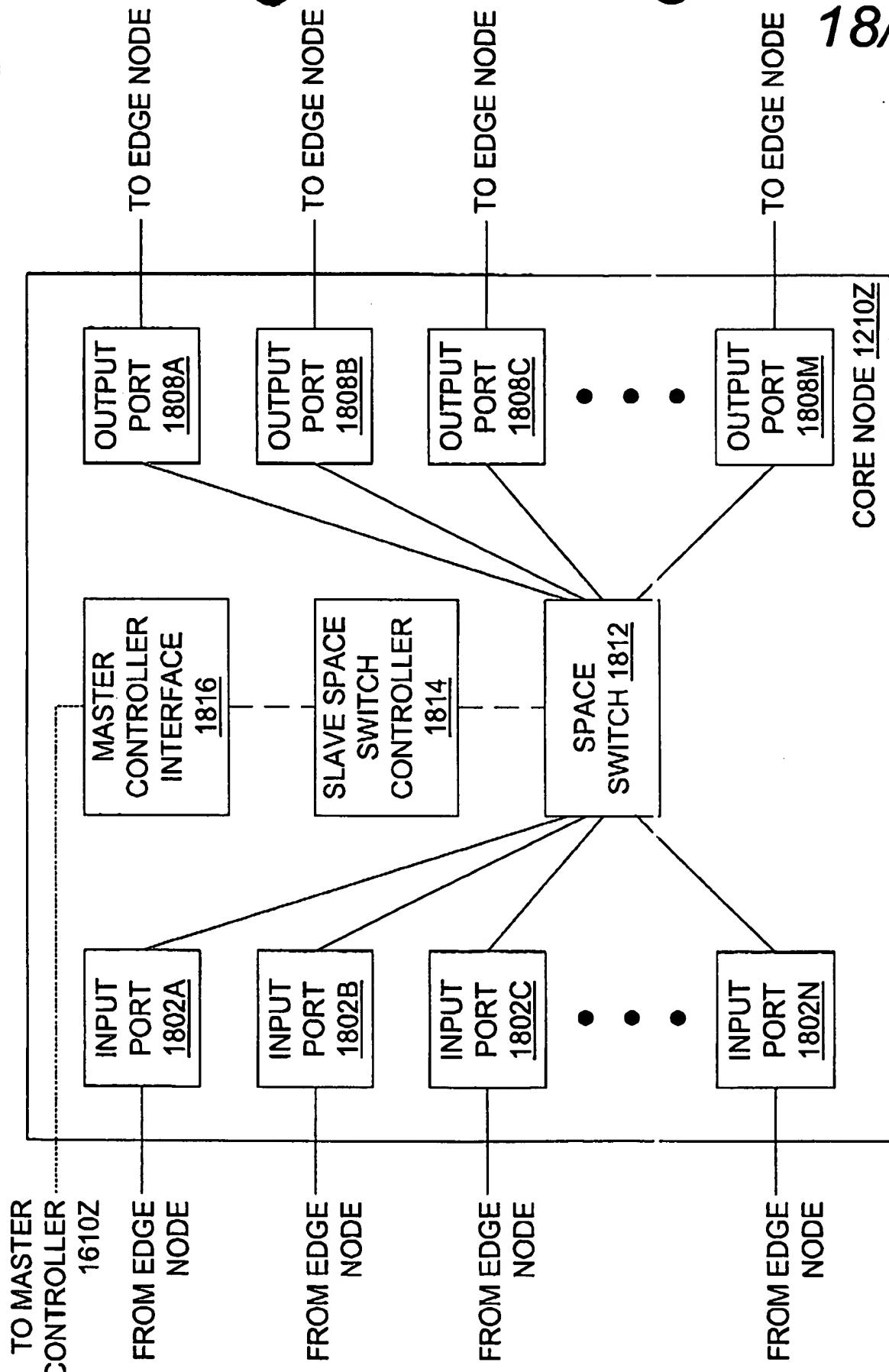
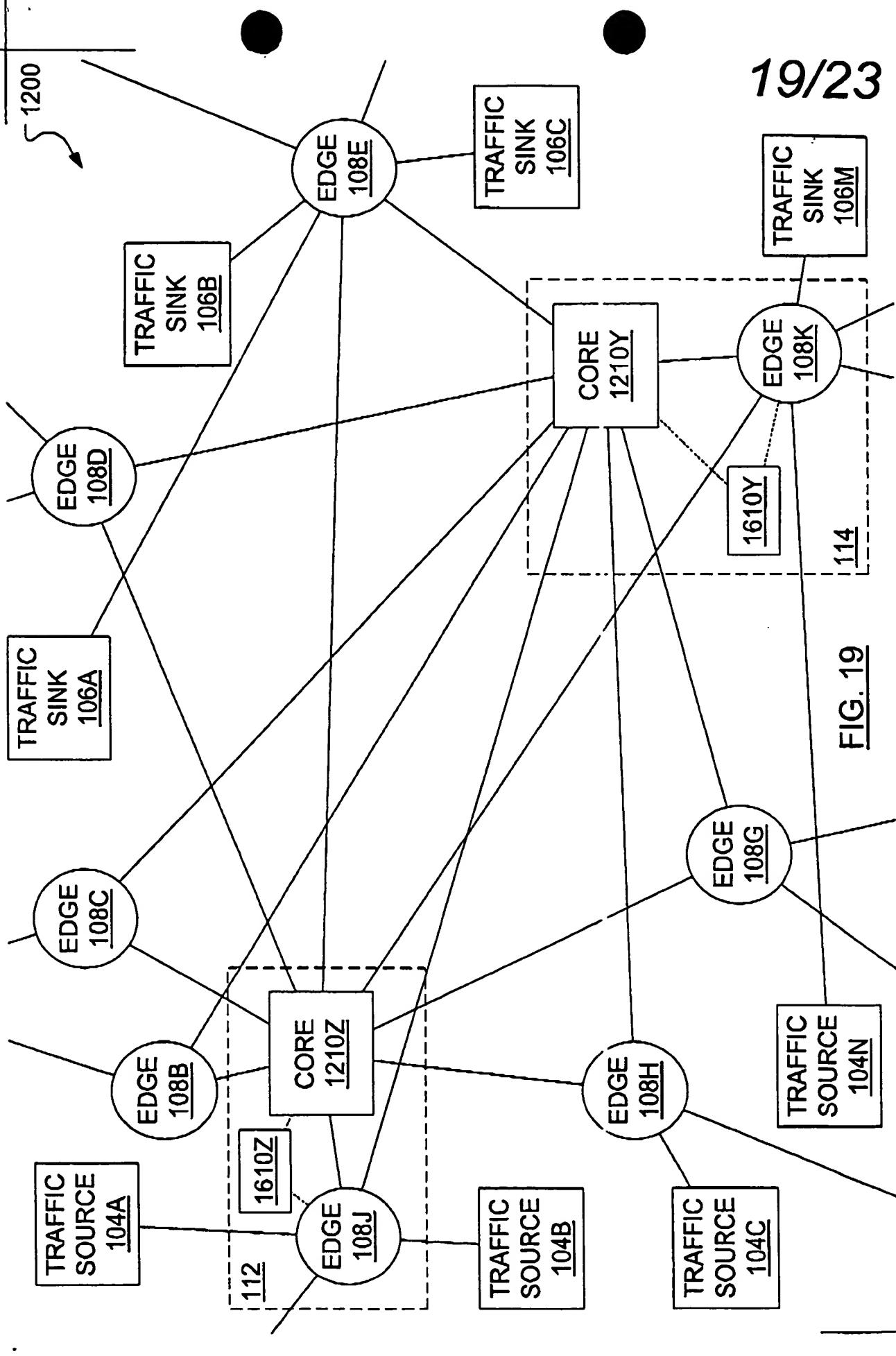
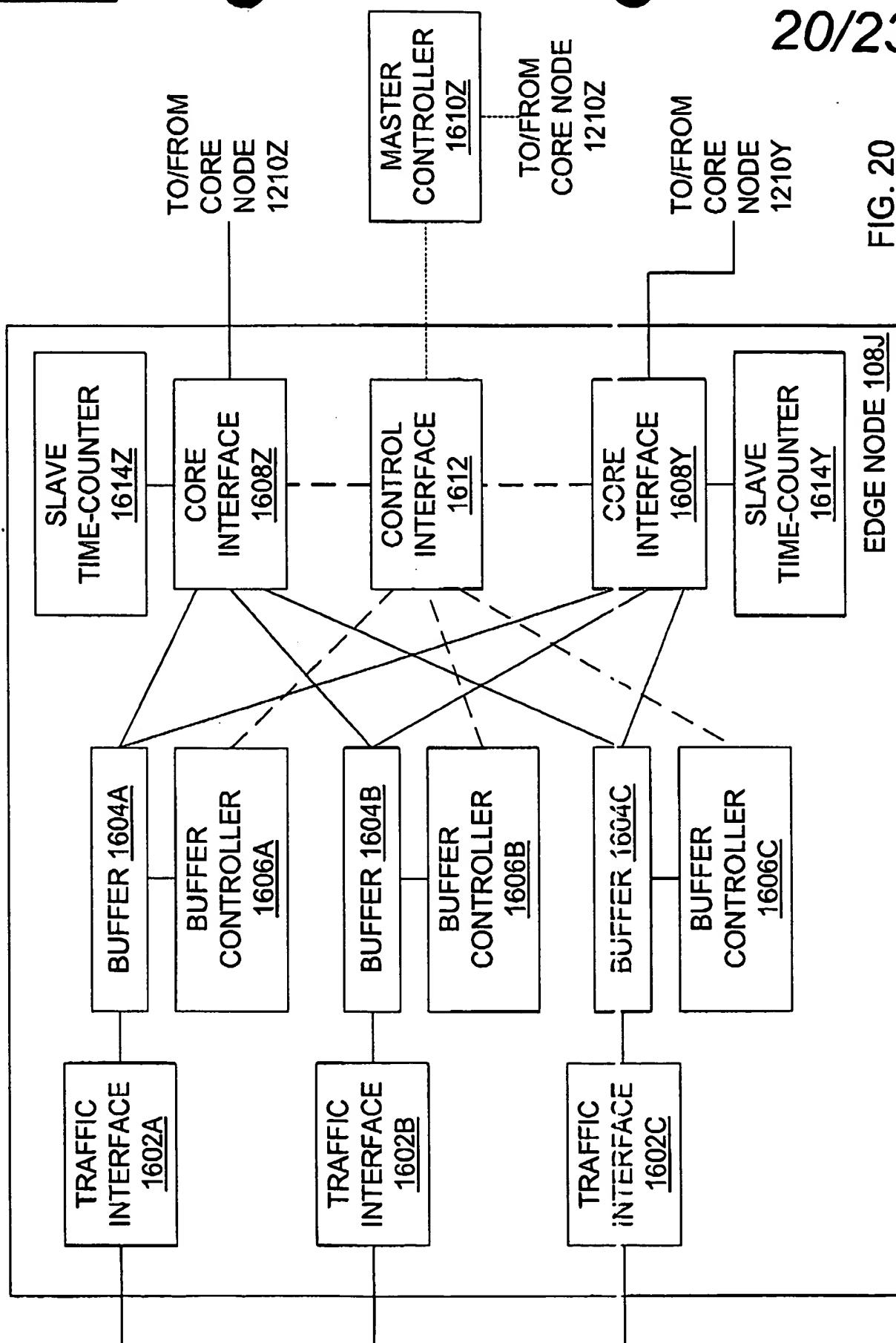
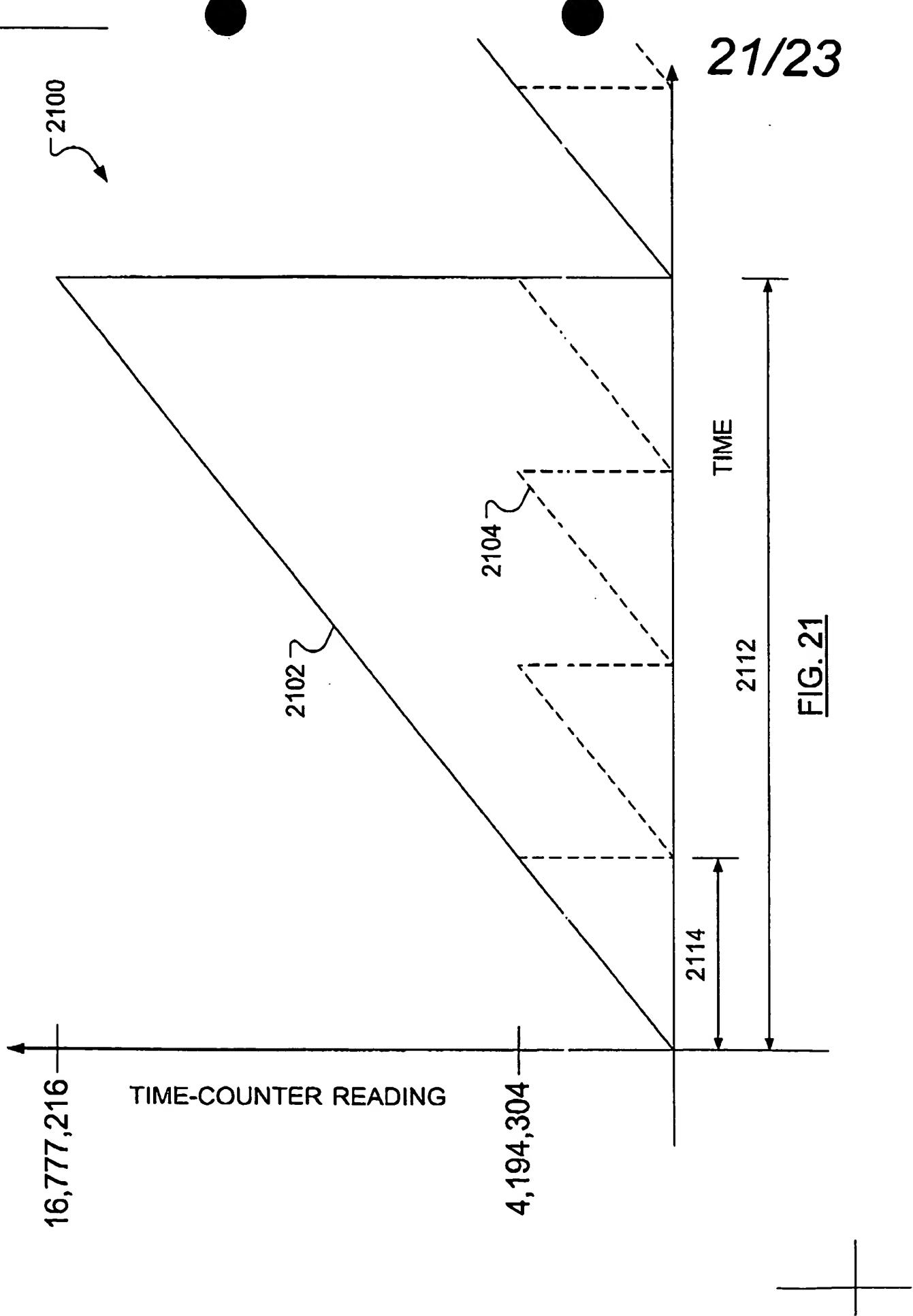


FIG. 18



20/23





22/23

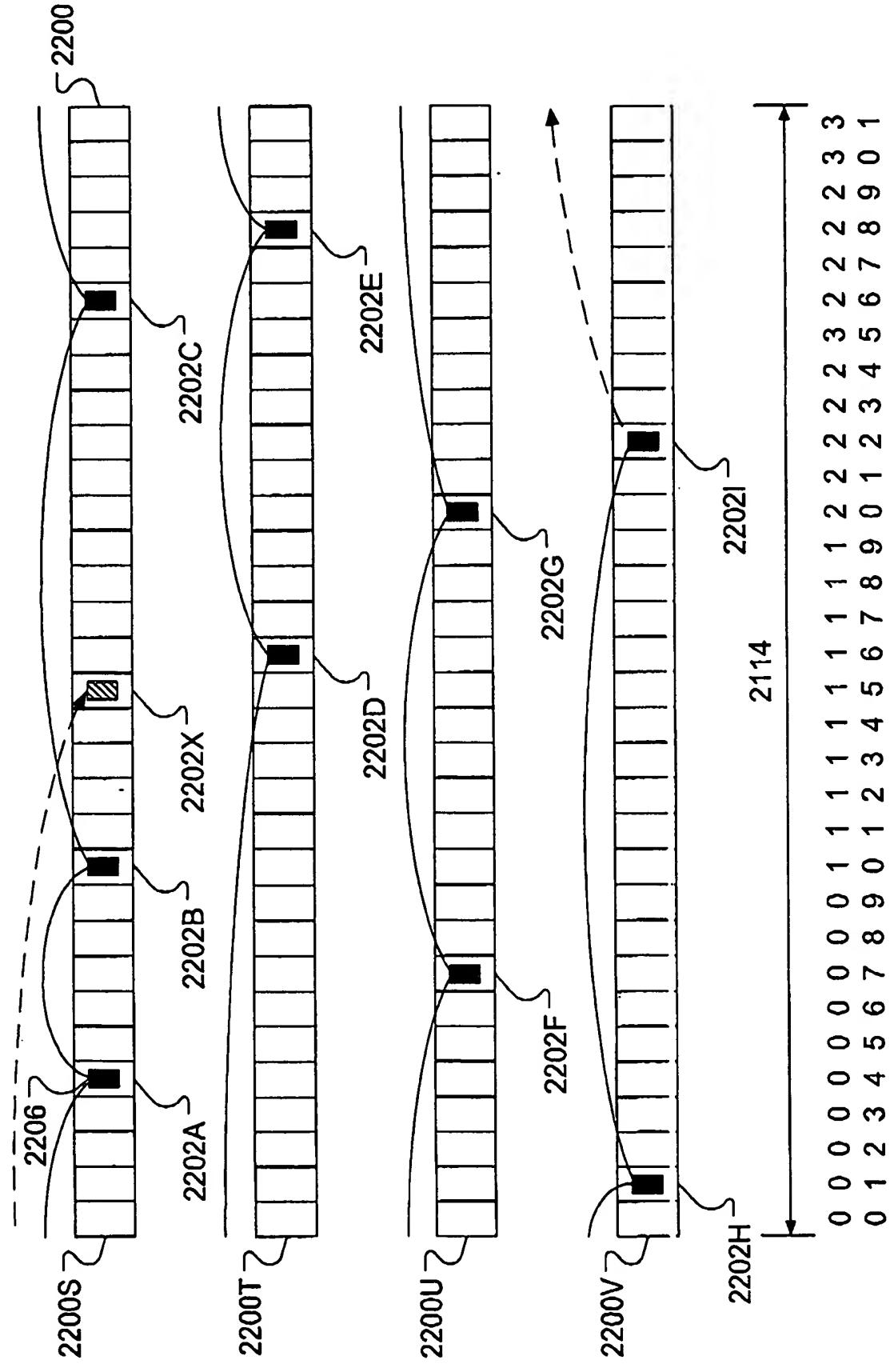
000000000011111122223333
01234567890123456789012345678901

FIG. 22

23/23

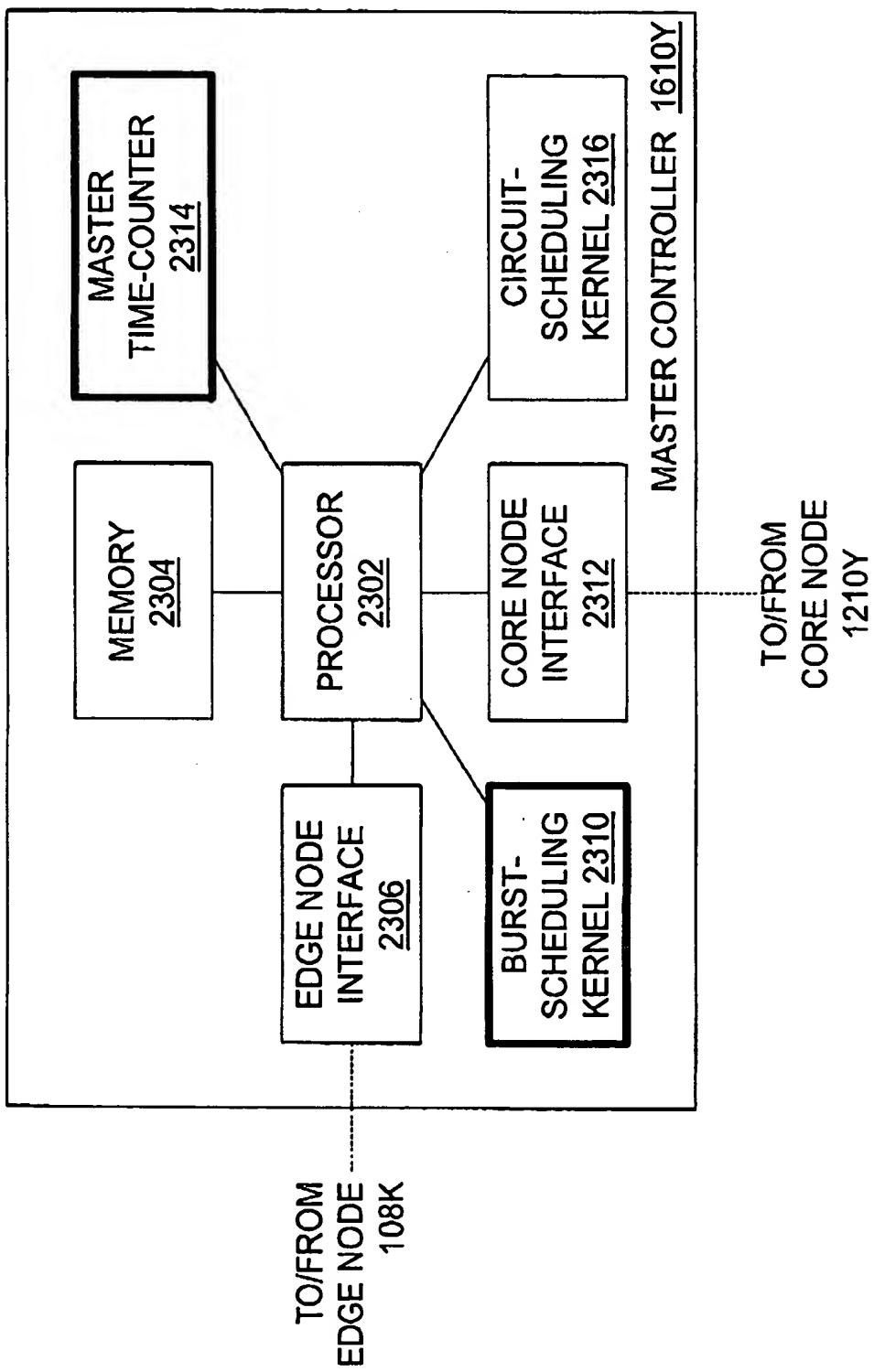


FIG. 23